

1 CLAIMS

- 2 1. A DC canceler circuit comprising:
- 3 a. a canceler input terminal adapted to receive a
4 series of data input samples;
- 5 b. a canceler output terminal adapted to provide a
6 series of data output samples;
- 7 c. a feedback path having:
- 8 i. a feedback-path input terminal connected to
9 the canceler output terminal;
- 10 ii. a feedback-path output terminal connected to
11 the canceler input terminal; and
- 12 iii. a sigma-delta modulator connected between the
13 feedback-path input terminal and the
14 feedback-path output terminal.

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